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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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**Complete if Known**

Application Number	10/769,201
Filing Date	01/29/2004
First Named Inventor:	Rotithor, et al.
Art Unit	2186
Examiner Name	Krofcheck, Michael
Attorney Docket Number	42P16971

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
MK		US- 5,208,914	05-1993	Wilson et al.	
		5,623,608	04-1997	Ng	
		6,012,106	01-04-2000	Schumann et al.	
		US- 6,091,618	07-18-2000	Fazio, et al.	
		6,157,990 A	12-05-2000	Randolph et al.	
		6,157,992 A	12-05-2000	Sawada et al.	
		6,233,656 B1	05-2001	Jones et al.	
		6,272,564 B1	08-07-2001	Garrett et al.	
		6,301,183	10-2001	Bondurant et al.	
		6,304,962 B1	10-16-2001	Nair	
		6,405,286 B1	06-11-2002	Gupta et al.	
		6,542,982	04-2003	Murakami et al.	
		US- 6,622,225 B1	09-2003	Kessler et al.	
		US- 6,651,148 B2	11-2003	Widdup	
		US-			

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		INTEL®, "3 Volt Fast Block Flash Memory, 28F800F3 and 28F160F3, January 2000, 51 pages.	
		INTEL®, Intel® Flash Data Integrator (FDI) User's Guide, Version 3.0, August 2000, 228 pages.	

Examiner  
Signature

/Michael Krofcheck/

Date  
Considered

09/21/2006

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Sheet	1	of	1
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